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The role of arithmetic in datapath design in VLSI design has been increasing in importance over the last several years due to the demand for processors that are smaller, faster, and dissipate less power. Unfortunately, this means that many of these datapaths will be complex both algorithmically and circuit wise.**

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Digital Computer Arithmetic Datapath Design Using Verilog ...

Digital Computer Arithmetic Datapath Design Using Verilog HDL. James E. Stine. This text presents basic implementation strategies for arithmetic datapath designs and methodologies utilized in the digital system. The author implements various datapath designs for addition, subtraction, multiplication, and division.

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